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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

JAG This application is a DIV of 10/127,583 04/23/2002 PAT 6,720,591

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAG JAPAN 2001-124475 04/23/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/12/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 56	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
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## TITLE

Semiconductor integrated circuit device and method of manufacturing the same

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